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Class	Subclass
ISSUE CLASSIFICATION	

PATENT NUMBER

U.S. UTILITY Patent Application

<i>KW</i> SCANNED <i>TR2</i> <i>or</i> <i>Am</i>	O.I.P.E.	PATENT DATE

APPLICATION NO. 09/987616	CONT/PRIOR	CLASS 361	SUBCLASS <i>Sila</i> <i>0-0.5</i>	ART UNIT 2836	EXAMINER <i>B. Brinson</i>
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APPLICANTS
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TITLE

Electrostatic discharge protection for a mixed-voltage device using a stacked-transistor- triggered silicon controlled rectifier

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)						
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
INTERNATIONAL CLASSIFICATION								

<input checked="" type="checkbox"/> TERMINAL DISCLAIMER <input checked="" type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____ <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	DRAWINGS Sheets Drwg. Figs. Drwg. Print Fig.			CLAIMS ALLOWED Total Claims Print Claim for O.G.	
	(Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
	(Primary Examiner) (Date)			ISSUE FEE Amount Due Date Paid	
	(Legal Instruments Examiner) (Date)			ISSUE BATCH NUMBER	

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